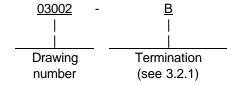
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| Sheet 1: Revisions Itr column; add "A". Revisions description column; add "Changes in accordance with NOR 5905-R009-03". Revisions date column; add "03-04-02". Revision level block; add "A". Rev status of sheets; for sheet 5, add "A". Sheet 8: Paragraph 6.6, Add, row 3 | | | | | | | | | | |
| | DSCC | drawing Ve | ndors similar de | esignation | Vend | or Vendor's name | e and | | | |
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| 14. THIS SE | ECTION F | FOR GOVERNI | IENT USE ONL | Y | | | | | | |
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| | | (2) Revised d | ocument must b | e received be | efore m | nanufacturer may ir | ncorporate this cha | nge. | | |
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| b. ACTIVITY AUTHORIZED TO APPROVE CHANGE FOR GOVERNMENT DSCC-VAT | | | | | c. TYPED NAME (I | Last) | | | | |
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| | | Andrew F | R. Err | st | | | | | COLUMBUS, OH | | | | | | | | | | |
| | | | HECKED BY ndrew R. Ernst | | | | TITLE RESISTOR, FIXED, FILM, CHIP, ZERO-OHM, STYLE 0505 | | | | | | | | | | | | |
| 10 December 2002 | | APPROV Kendall A | | | <u> </u> | | | | | | | | | | | | | | |
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- 1. SCOPE
- 1.1 Scope. This drawing describes the requirements for a fixed, film, .050 X .050, zero-ohm, chip resistor.
- 1.2 Part or Identifying Number (PIN). The complete PIN is as follows and is available in wrap-around termination:



2. APPLICABLE DOCUMENTS

2.1 Government documents.

2.1.1 <u>Specifications, standards, and handbooks</u>. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation (see 6.2).

SPECIFICATIONS

DEPARTMENT OF DEFENSE

MIL-PRF-55342 - Resistors, Fixed, Film, Chip, Nonestablished Reliability, Established Reliability, Space Level, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-790 - Established Reliability and High Reliability Qualified Products List (QPL) Systems for Electrical, Electronic, and Fiber Optic Parts Specification.

MIL-STD-1285 - Marking of Electrical and Electronic Parts.

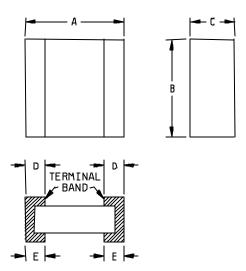
(Unless otherwise indicated, copies of federal and military specifications, standards, and handbooks are available from the Document Automation and Production Service, Building 4D (DPM-DODSSP), 700 Robbins Avenue, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

- 3.1 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-55342 and as specified herein.
- 3.2 <u>Interface and physical dimensions</u>. The resistor shall meet the interface and physical dimensions as specified in MIL-PRF-55342 and herein (see figure 1).
- 3.2.1 <u>Termination material</u>. The termination material shall be in accordance with MIL-PRF-55342 and code letters B, C, G, or U (see 6.4).

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| Α | В | С | D | Е |
|-------|-------|-----------|-------|-------|
| .055 | .050 | .012/.033 | .010 | .015 |
| ±.006 | ±.005 | | ±.005 | ±.005 |

| .005 | 0.13 |
|------|------|
| .006 | 0.15 |
| .010 | 0.25 |
| .012 | 0.30 |
| .015 | 0.38 |
| .033 | 0.84 |
| .050 | 1.27 |
| .055 | 1.40 |
| | |

mm

Inches

NOTES:

1. Dimensions are in inches.

2. Metric equivalents are given for general information only.

3. The pictorial view of the style above is given as representative of the item.

Slight deviations from the outline shown, which are contained within the envelope, and do not alter the functional aspect of the device, are acceptable.

FIGURE 1. Zero-ohm chip resistor style 0505.

3.3 Electrical characteristics.

- 3.3.1 Temperature range. The temperature range shall be -55°C to +125°C.
- 3.3.2 Resistance. The resistance shall not exceed 0.025 ohm.
- 3.4 <u>Marking</u>. Marking of the individual chip resistors is not required; however, each unit package shall be marked in accordance with MIL-STD-1285 and include the PIN as specified herein (see 1.2), the manufacturer's name or Commercial and Government Entity (CAGE) code, date, and lot codes.

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- 3.5 <u>Recycled, recovered, or environmentally preferable materials</u>. Recycled, recovered, or environmentally preferable materials should be used to the maximum extent possible provided that the material meets or exceeds the operational and maintenance requirements, and promotes economically advantageous life cycle costs.
- 3.6 <u>Certificate of compliance</u>. A certificate of compliance shall be required from manufacturers requesting to be a suggested source of supply.
- 3.7 <u>Workmanship</u>. Resistors shall be uniform in quality and free from defects that will affect life, serviceability, or appearance.

4. VERIFICATION

- 4.1 <u>Product assurance program</u>. The product assurance program specified in MIL-PRF-55342 and maintained in accordance with MIL-STD-790 is not applicable to this document.
 - 4.2 Qualification inspection. Qualification inspection is not applicable to this document.
- 4.3 <u>Product level qualification</u>. The product level qualification specified in MIL-PRF-55342 is not applicable to this document.
 - 4.4 Conformance inspection.
- 4.4.1 <u>Inspection of product for delivery</u>. Inspection of product for delivery shall consist of group A inspection (established reliability level only and the parts per million reporting is not applicable) of MIL-PRF-55342.
- 4.5 <u>Visual and mechanical examination</u>. Resistors shall be examined to verify that the materials, design, construction, physical dimensions, marking, and workmanship are in accordance with the applicable requirements of MIL-PRF-55342.

5. PACKAGING

5.1 <u>Packaging</u>. For acquisition purposes, the packaging shall be as specified in the contract or order (see 6.2) When actual packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department or Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Departments or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory.)

- 6.1 <u>Intended use</u>. Chip resistors are intended for use in thick or thin film circuits where microcircuitry is intended; also in most surface mount applications.
 - 6.2 Ordering data. The contract or purchase order should specify the following:
 - a. Complete PIN (see 1.2).
 - b. Requirements for delivery and one copy of the conformance inspection data or certification of compliance that parts have passed conformance inspection with each shipment of parts by the manufacturer.
 - c. Requirements for packaging and packing.
- 6.3 <u>Users of record</u>. Coordination of this document for future revisions is coordinated only with the suggested sources of supply and the users of record of this document. Requests to be added as a recorded user of this drawing should be in writing to: Defense Supply Center, Columbus, DSCC-VAT, Post Office Box 3990, Columbus, OH 43216-5000 or by telephone (614) 692-0552 or DSN 850-0552.

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6.4 <u>Suggested source of supply</u>. A suggested source of supply is listed herein. Additional sources will be added as they become available. For assistance in the use of this drawing, contact Defense Supply Center, Columbus, DSCC-VAT, Post Office Box 3990, Columbus, OH 43216-5000 or by telephone (614) 692-0552 or DSN 850-0552.

| DSCC drawing PIN 03002-* | Vendors similar designation or type number <u>1</u> / | Vendor CAGE | Vendor's name and address |
|----------------------------------|---|----------------|--|
| Terminal styles B, C, G, or U | H0505CP*000(D03002) | 56235 | State of the Art, Incorporated 2470 Fox Hill Road State College, PA 16803-1797 |
| Terminal styles B | RCWPM-550-99 | 91637 | Vishay Dale Electronics PO Box 609 Columbus, NE 68602-0609 |

1/ Parts must be purchased to the DSCC PIN to assure that all performance requirements and tests are met.

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